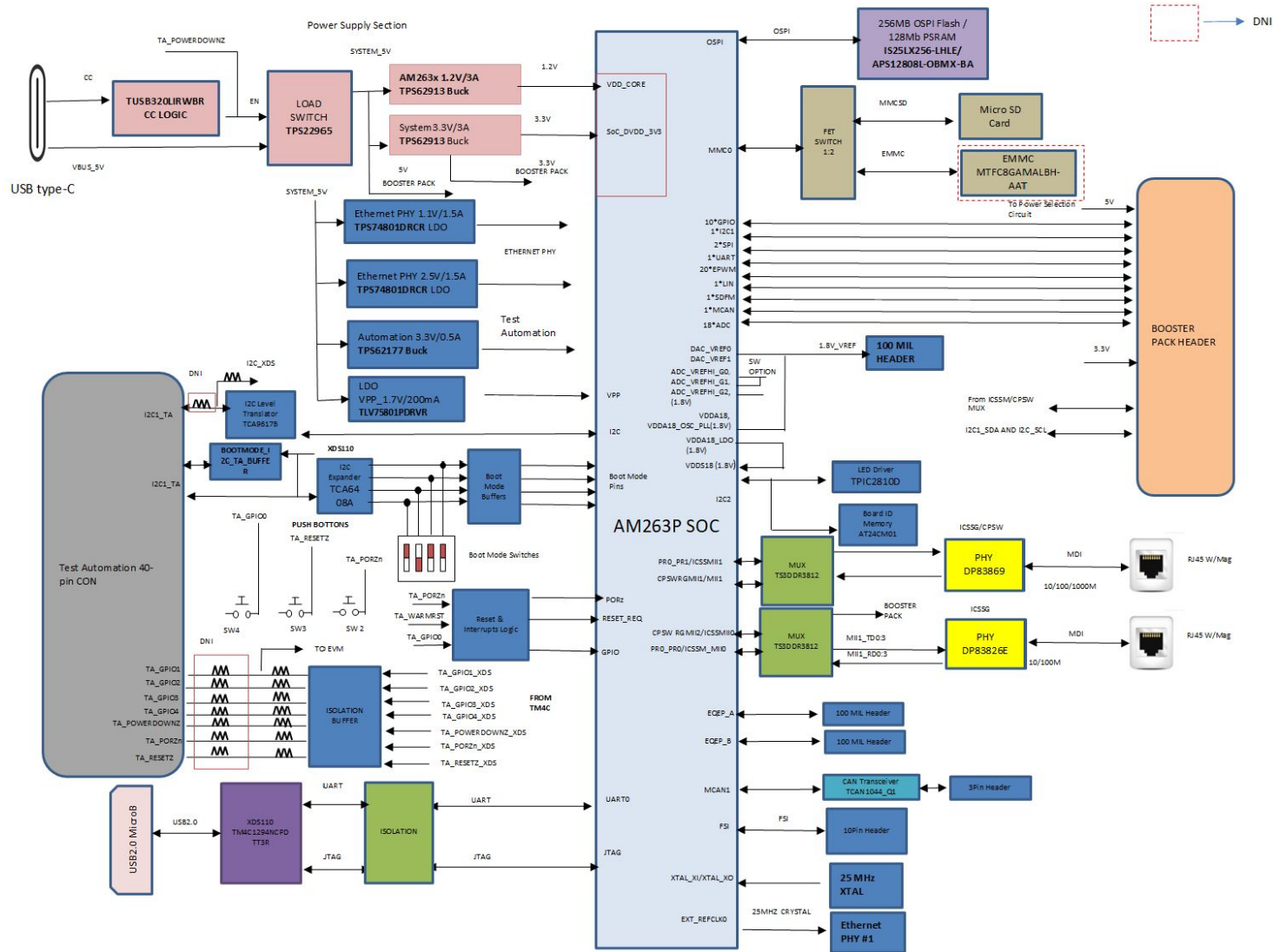


AM263P Launchpad

System Block Diagram



Feature List

- TI Launchpad XL Form-factor
- AM263P MCU+
- Dual R5F ARM Safety Cores
- 5Mbyte SRAM
- 256Mbyte OSPI Flash Memory
- 1Mbyte I2C EEPROM Memory (Board ID)
- 8GB EMMC Interface

Analog

- 20x ADC input channels
- 1x DAC output Channels

Control

- 10x EPWM
- 2x EQEP encoder input channels
- 2x SFDMA Resolver input channels
- 4x FSI Channels

Connectivity

- 2x UART
- 2x SPI
- 2x I2C

Industrial/Automotive Networking

- 2x PRU(Programming Real-time Units)
- 2x MCAN
- 2x LIN
- 2x RGMII/RMII Industrial Ethernet

Development

- Isolated, Embedded XDS110 JTAG/UART
- TIVA Support

Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 19-03-2025
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title:
Drawn By: Shrinivas	Assembly Variant: 001
Engineer: Shrinivas	File: PROC171_CoverSheet.SchDoc
	Sheet: 1 of 27
	Size: B
	http://www.ti.com
	© Texas Instruments

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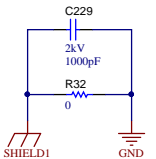
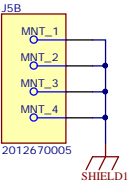
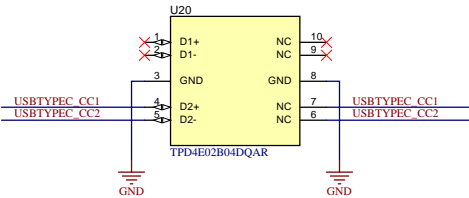
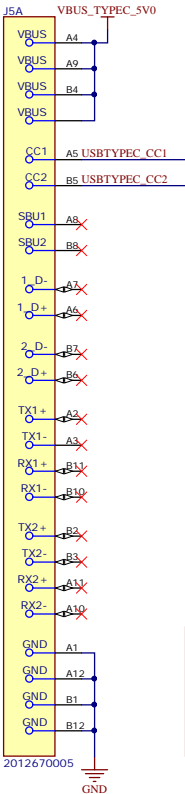
Revision History

Rev	ECN #	Approved Date	Approved by	Notes
E1	N/A	23/05/2023	Ajit M B	Updated schematics as per TI comments
E2	N/A	06/12/2023	Ajit M B	MX25LW25645GXDQ00 OSPI part Changed to IS25LX256-LHLE as per TI comments
E2A	YES	12/03/2024	Ajit M B	OSPI DQS and OSPI LBCLK net names are swapped mount R382 DNI R375 AND R381
A	N/A	23/08/2024	Ajit M B	OSPI DQS and OSPI LBCLK net names are swapped to UART1_TXD and UART1_RXD as per the Datasheet AM263P_EMMC0_CLK_MUX and AM263P_EMMC0_CMD_MUX are swapped as per the eMMC chip BOOSTXL-IOLINKM-8 EVM booster pack support is provided as per the TI comments Support for PSRAM has been added to the current OSPI part, along with additional path for CS1
A	N/A	12/05/2025	Ajit M B	IS25LX256-LHLE OSPI part Changed to IS25LX256-JHLE

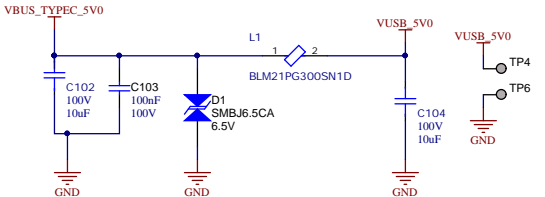
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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 12-05-2025	
TID #: N/A	Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 27
Drawn By:	File: PROC171_Revision_History.SchDoc	Size: B
Engineer: Shrinivas	Contact:	http://www.ti.com

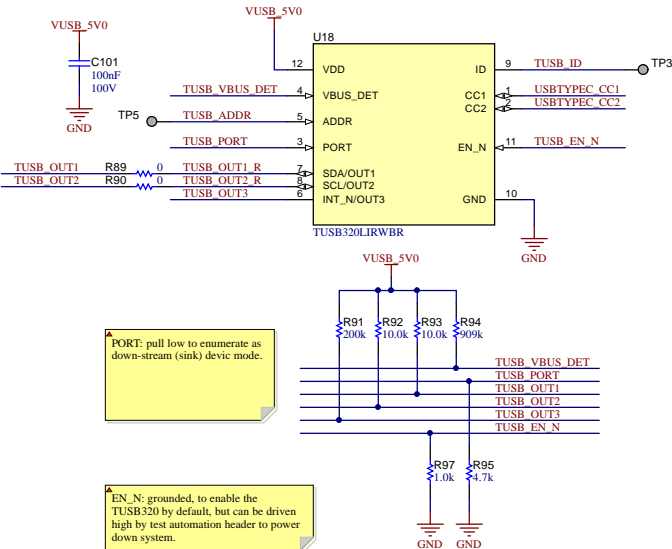
USB Type-C Power Input: 5.0V, 3.1A



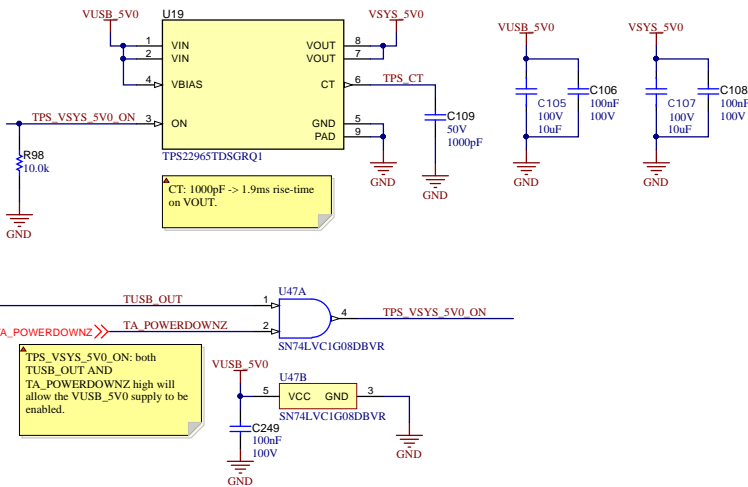
USB 5.0V Input Power Filtering



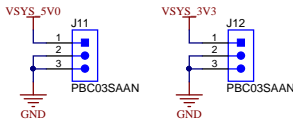
USB Type-C CC Logic Controller



USB 5.0V Input Power Load Switch (4A max)



Boosterpack Extended Power



USB Type-C DFP CC Emulation



TUSB_OUT[2:1]: both driven low when a 3A source presented to the USB320 CC controller.

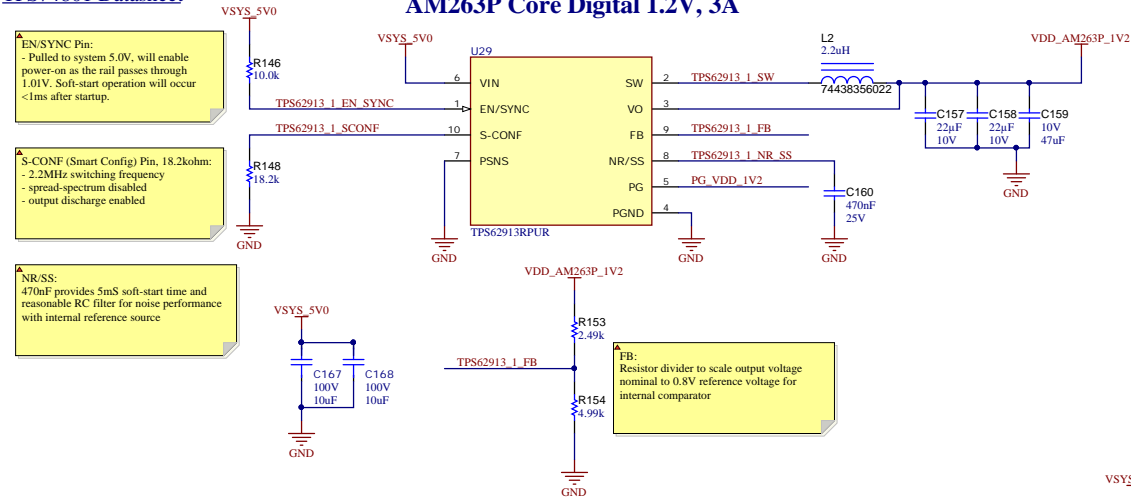
TPS_VSYS_5V0_ON: both TUSB_OUT AND TA_POWERDOWNZ high will allow the VUSB_5V0 supply to be enabled.

Install 10kohm resistor into R439 to allow for powering the VUSB_5V0 from the BoosterPack J10/J14 jumpers. Booster Pack power pins must supply a valid 5.0V/3A supply.

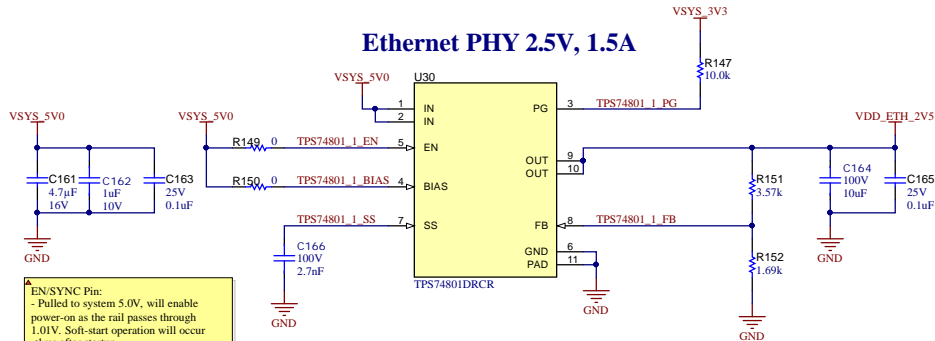
Note: Installing R439 will prevent the board from being powered through the J5 USB Type-C Connector when using a valid USB Type-C host port.

TPS62913 Datasheet
TPS74801 Datasheet

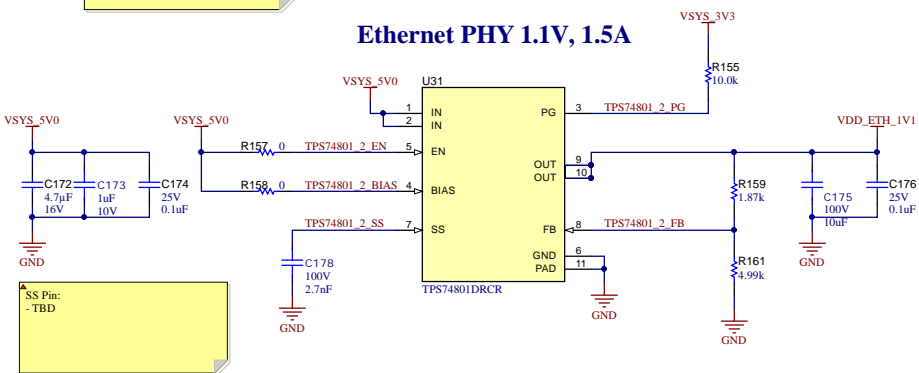
AM263P Core Digital 1.2V, 3A



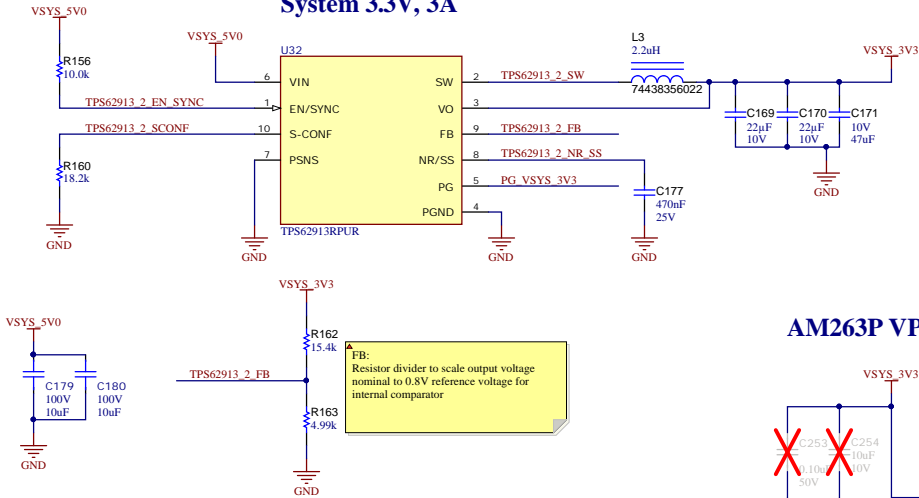
Ethernet PHY 2.5V, 1.5A



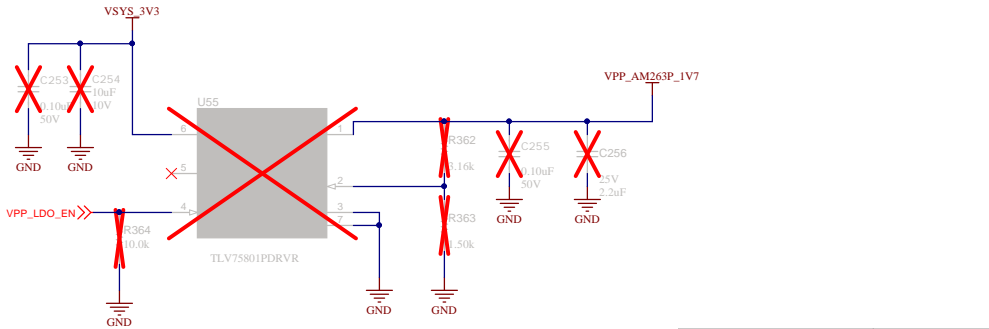
Ethernet PHY 1.1V, 1.5A



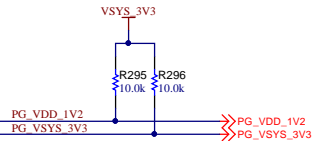
System 3.3V, 3A



AM263P VPP 1.7V, 0.5A



1.2V, 3.3V Power-Good

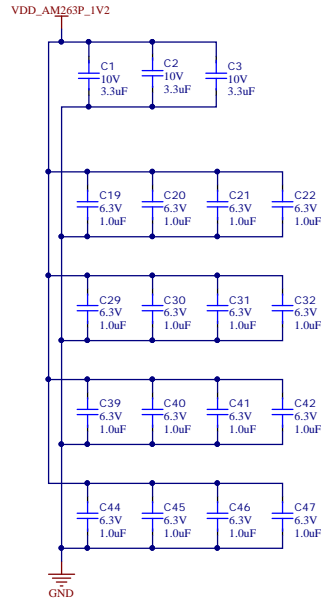


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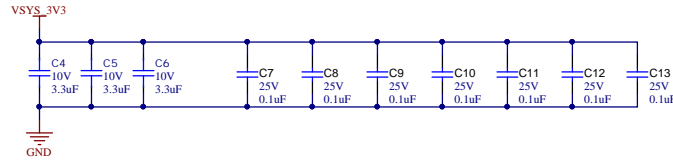
Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: Assembly Variant: 001
Drawn By: Engineer: Shrinivas	File: PROC171_System Power_2.SchDoc
	Sheet: 4 of 27
	Size: B
	Contact: http://www.ti.com



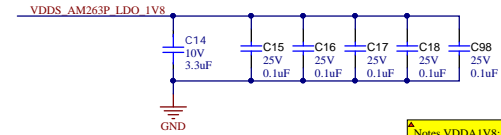
VDD 1V2 Core Digital



VDDS 3V3 Digital

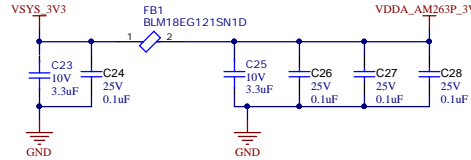


VDDS 1V8 Digital



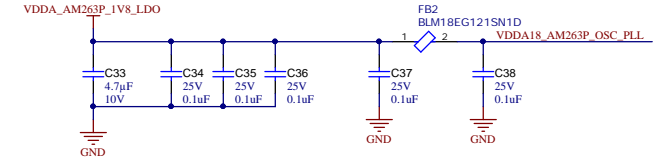
Notes VDDA1V8:
- May not need ferrite filtering, but will keep this for experimenting

VDDA 3V3 Analog



Notes VDDA33:
- May not need ferrite filtering, but will keep this for experimenting

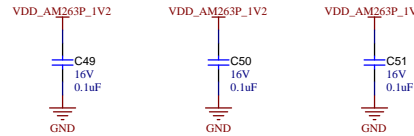
VDDA 1V8 Analog



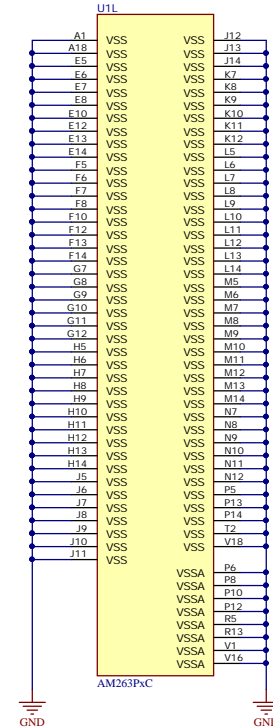
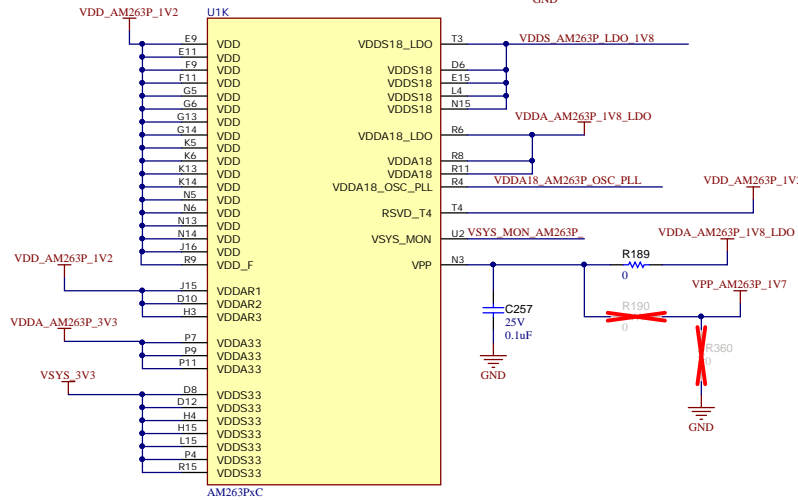
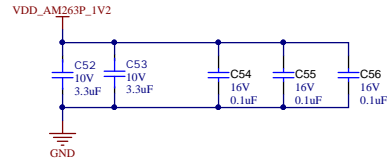
VSYS Voltage Monitor

Notes: VSYS_MON
- Vmax 1.8V
- scaling 4.25V (5V -15%) to 0.896V for 0.9V comparator input

VNWA 1V2 VDD_F 1V2 VDDA 1V2 Temperature



VDDAR[3:1] 1V2 SRAM Array

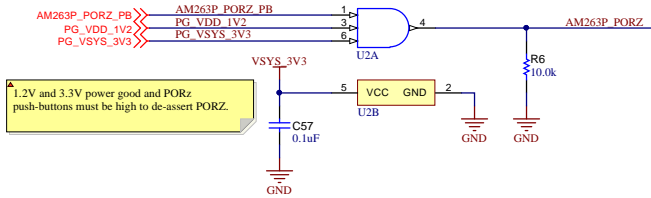


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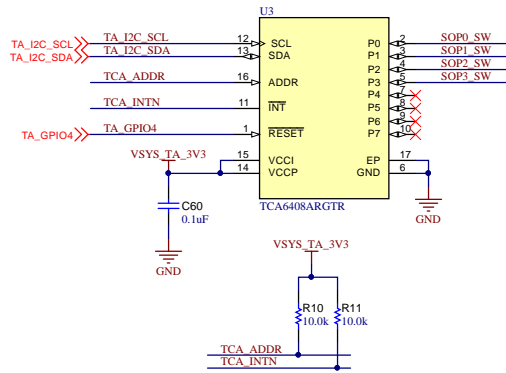
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TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: Assembly Variant: 001
Drawn By: Engineer: Shrinivas	File: PROC171_AM263P_1_Power.SchDoc
	Sheet: 5 of 27
	Size: B
	http://www.ti.com
	© Texas Instruments

AM263P Clock, Reset, Boot, JTAG

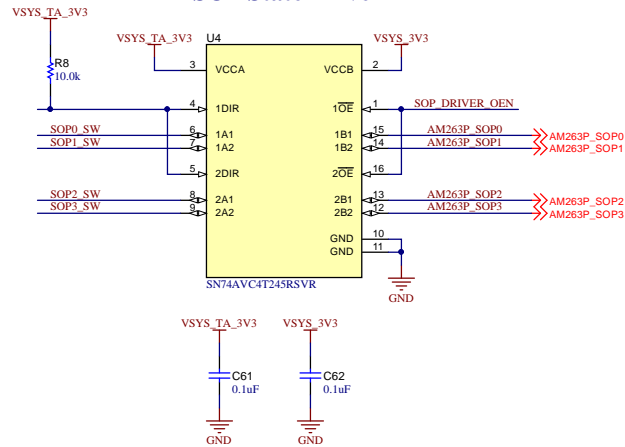
POR Generation



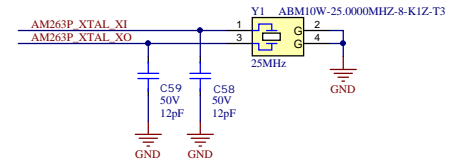
Test Automation SOP Select



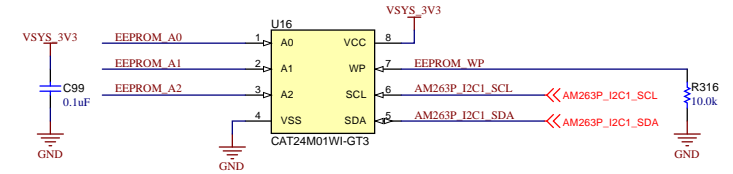
SOP State Driven



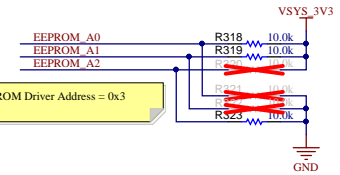
25 MHz Crystal



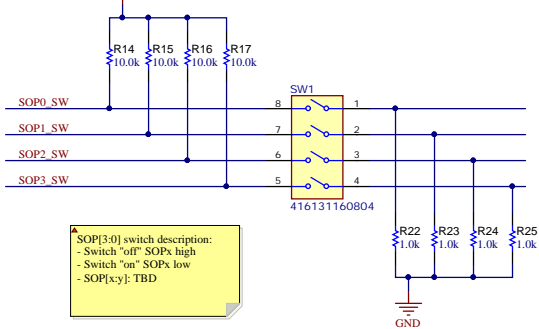
Board ID EEPROM



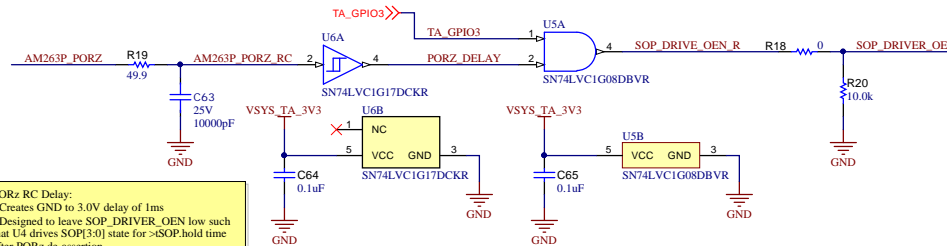
EEPROM Address



DIP Switch SOP Select



PORZ SOP Driver RC Delay



FORM	TA_OPTS	Resource Control from Test Automation MGR
HIGH	LOW	Enabled
HIGH	HIGH	Disabled

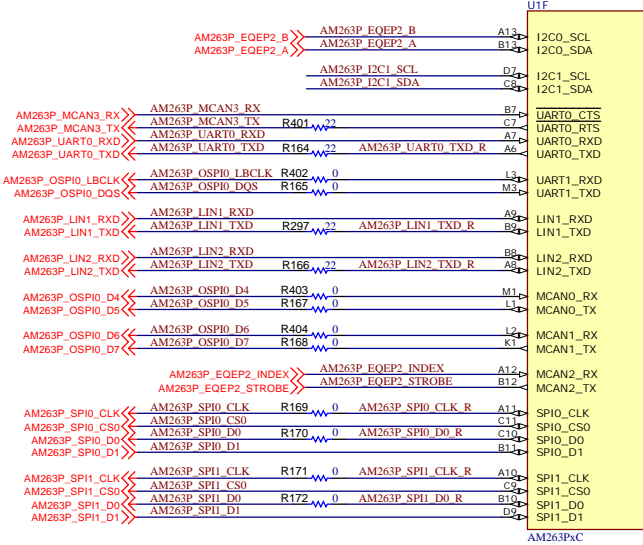
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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS	Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 27
Drawn By:	File: PROC171_AM263P_2_Clock_Reset_Boot_JTAG_SchDoc	
Engineer: Shrinivas	Contact:	

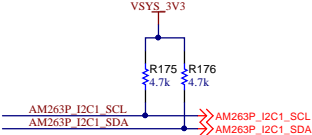


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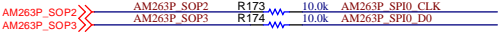
AM263P Serial Connectivity



I2C1 Pull-Up



AM263P SOP[3:2]

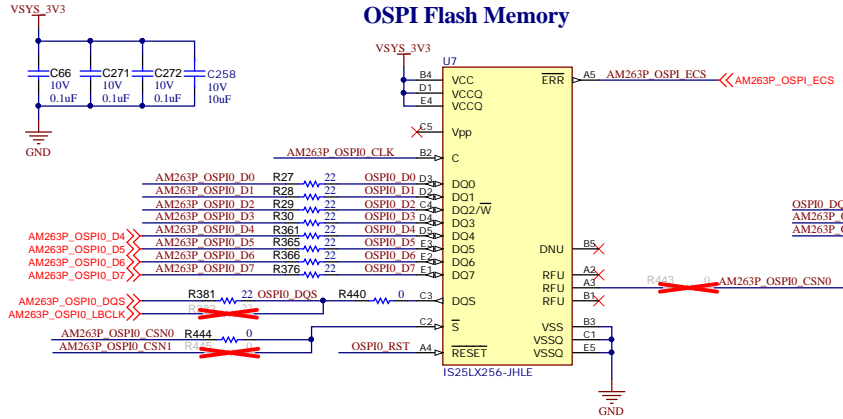


Note: 10kohm resistors placed near device to isolate SOP path during functional usage of peripherals. Place near AM263Px BGA.

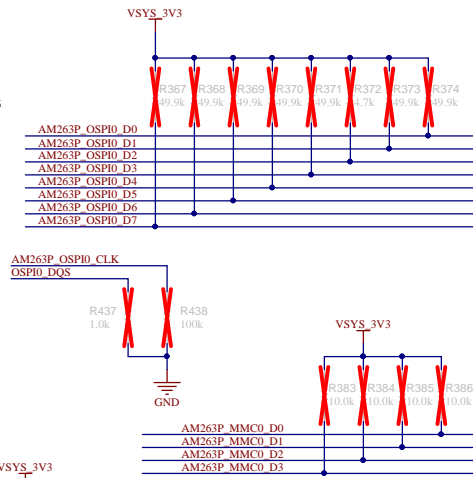
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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 15-05-2025
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: XDS110 JTAG/USB-to-UART Bridge
Drawn By: Shrinivas	Assembly Variant: 001
Engineer: Shrinivas	File: PROC171_AM263P_Serial_Connectivity_SchDdSize: B
	Contact: http://www.ti.com

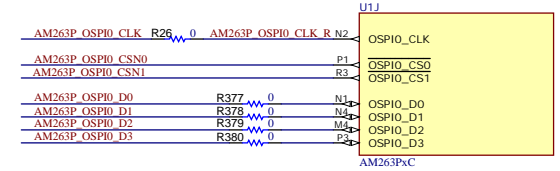
OSPI Flash Memory



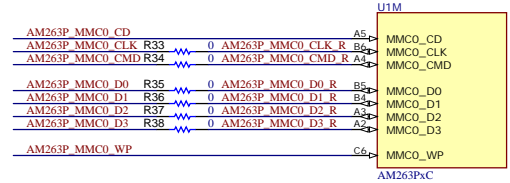
AM263P OSPI and MMC



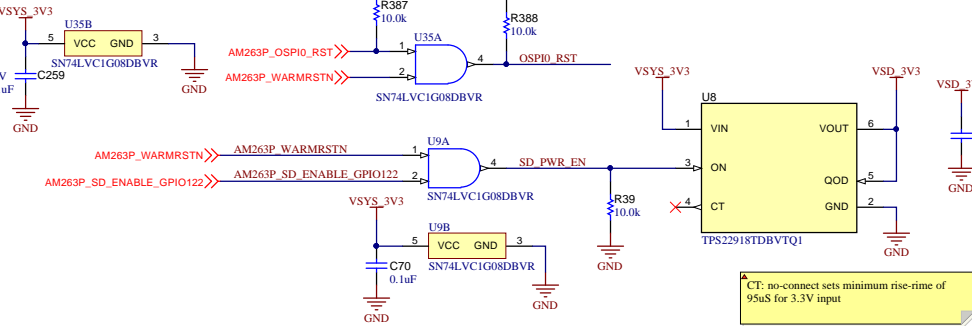
AM263P OSPI



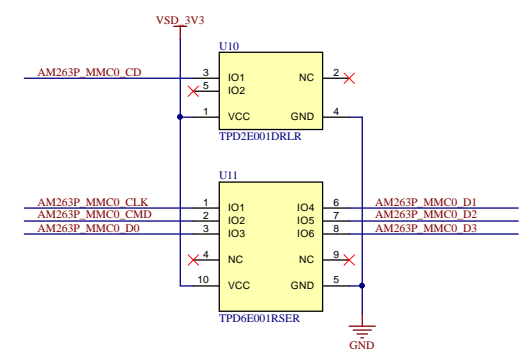
AM263P MMC0



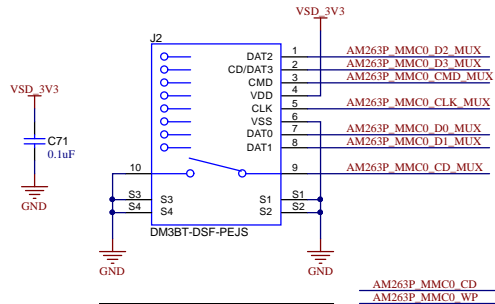
Micro-SD Power Switch



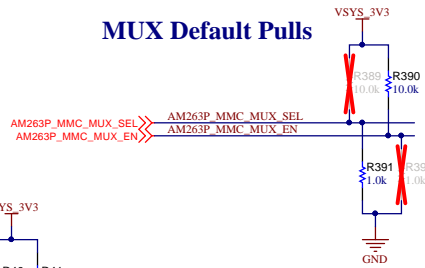
Micro-SD ESD



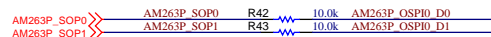
Micro-SD Card Socket



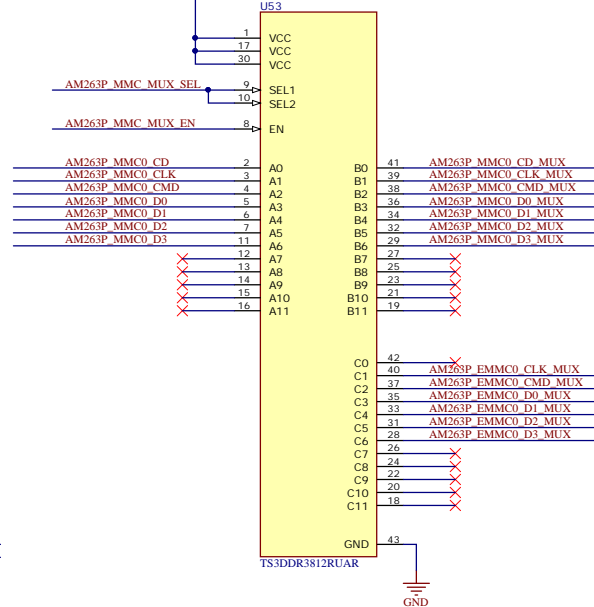
MUX Default Pulls



AM263P SOP[1:0]



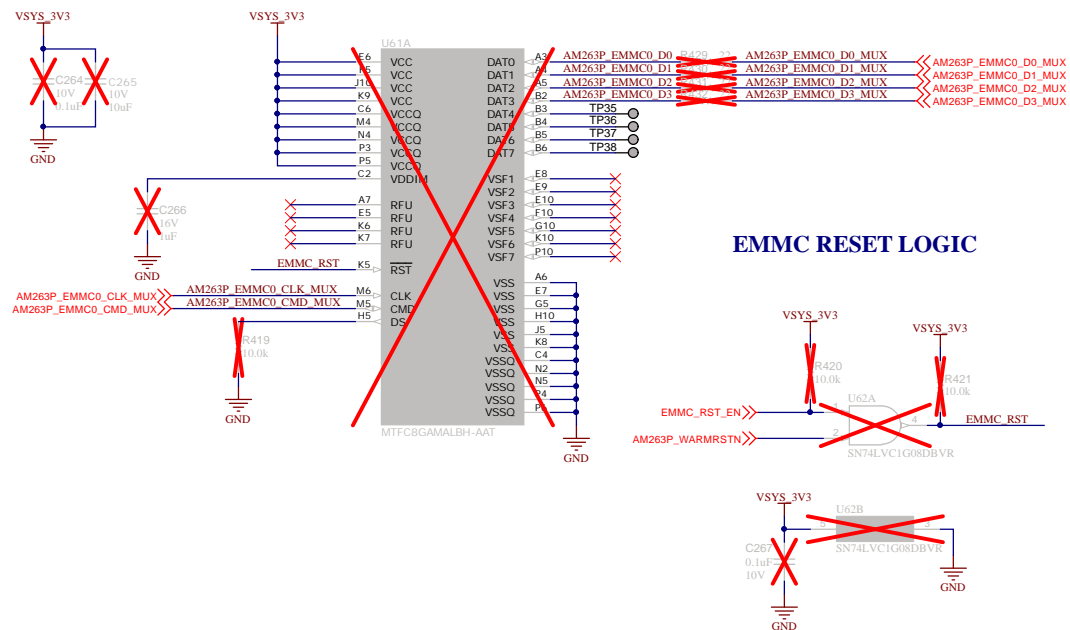
Note: 10kohm resistors placed near device to isolate SOP path during functional usage of peripherals. Place near AM263Px BGA.



EN	SEL1	SEL2	FUNCTION
L	X	X	A ₀ to A ₁₁ , B ₀ to B ₁₁ , and C ₀ to C ₁₁ are Hi-Z
H	L	L	A ₀ to A ₉ = B ₀ to B ₉ and A ₁₀ to A ₁₁ = B ₁₀ to B ₁₁
H	L	H	A ₀ to A ₉ = B ₀ to B ₉ and A ₁₀ to A ₁₁ = C ₀ to C ₁₁
H	H	L	A ₀ to A ₉ = C ₀ to C ₉ and A ₁₀ to A ₁₁ = B ₀ to B ₁₁
H	H	H	A ₀ to A ₉ = C ₀ to C ₉ and A ₁₀ to A ₁₁ = C ₁₀ to C ₁₁

Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 12-05-2025
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Assembly Variant: 001
Drawn By: Shrinivas	File: PROC171_AM263P_3_OSPI_MMC.SchDoc
Engineer: Shrinivas	Contact: Size: B

AM263P EMMC

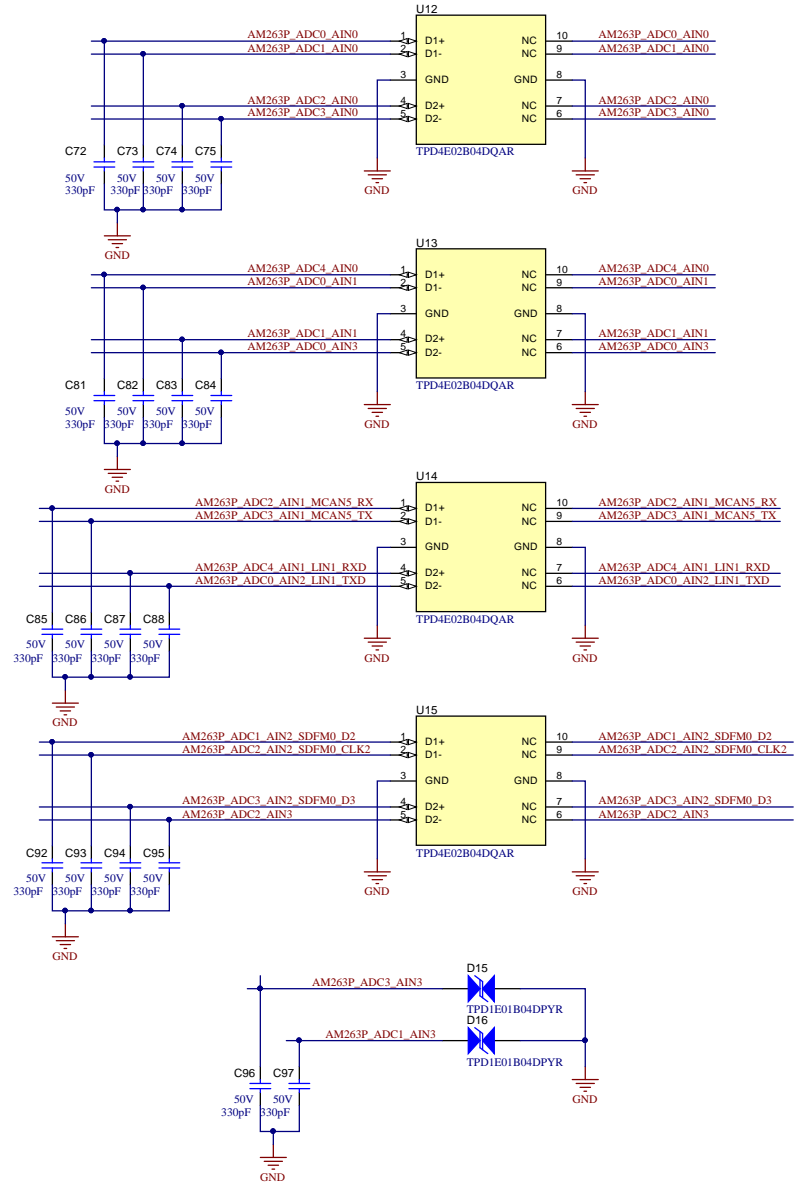
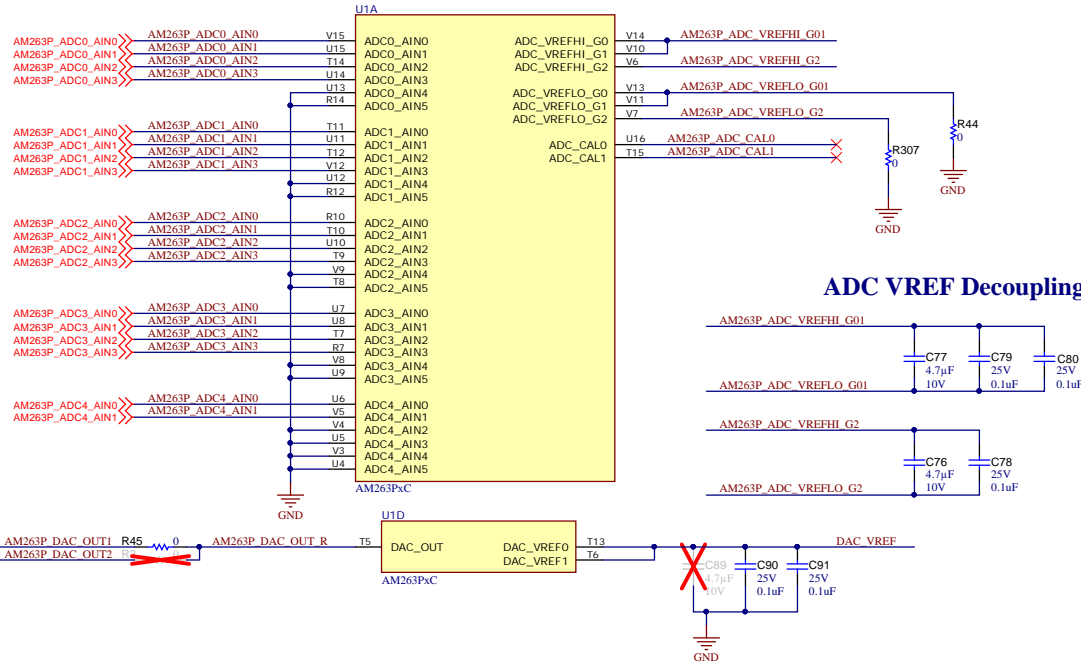


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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS	Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title: *
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 9 of 27
Drawn By: *	File: PROC171_AM263P_4_EMMC.SchDoc	Size: B
Engineer: Shrinivas	Contact:	



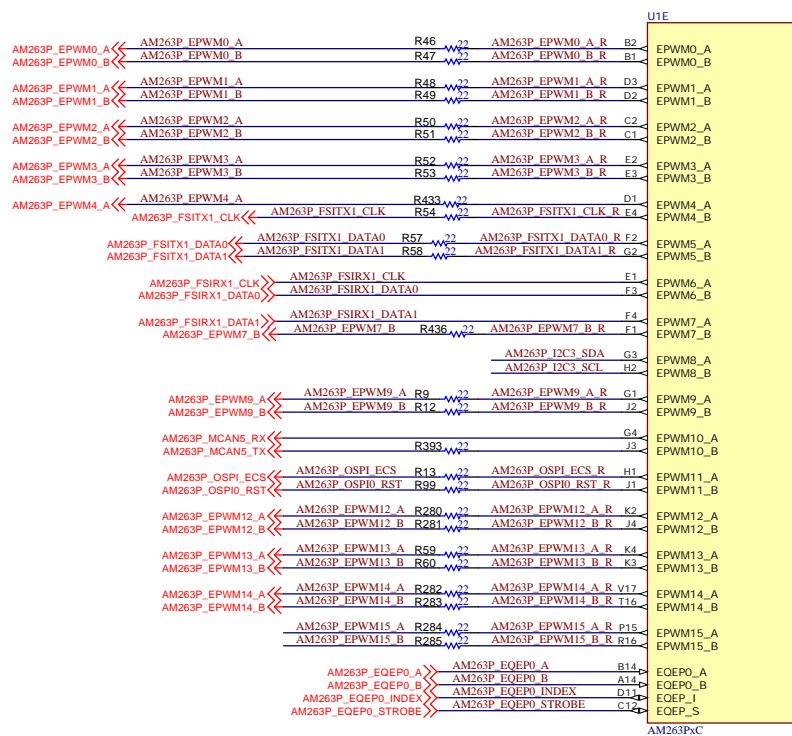
AM263P ADC and DAC



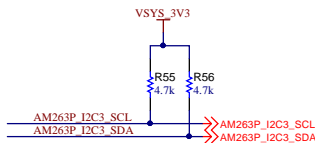
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Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: Assembly Variant: 001
Drawn By: Shrinivas	File: PROC171_AM263P_5_ADC_DAC_SchDoc
Engineer: Shrinivas	Contact: Size: B

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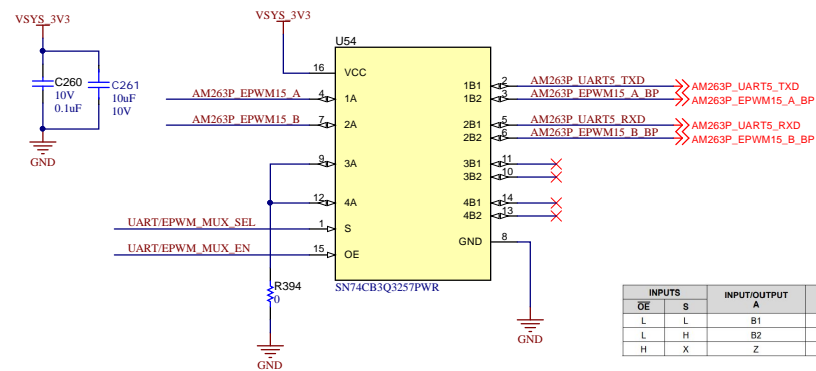
AM263P ePWM, eQEP, FSI



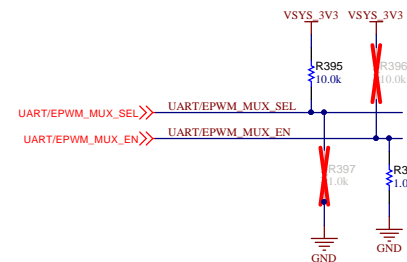
I2C3 Pull-Up



BOOSTER_PACK_MUX



INPUTS		INPUT/OUTPUT A	FUNCTION
OE	S		
L	L	B1	A port = B1 port
L	H	B2	A port = B2 port
H	X	Z	Disconnect



1	2	3	4	5	6
---	---	---	---	---	---

A





Pin connection diagram for the AM2639PDC module. The diagram shows the module's pins (R1-R70) connected to a breadboard. Key connections include:

- MDIO_CPSW (R1) to MDIO0_MDC (M17) and MDIO0_MDIO (M16)
- MDIO_CPSW (R2) to MDIO0_MDIO (M16)
- MDIO_CPSW (R3) to MDIO0_MDIO (M16)
- MDIO_CPSW (R4) to MDIO0_MDIO (M16)
- MDIO_CPSW (R5) to MDIO0_MDIO (M16)
- MDIO_CPSW (R6) to MDIO0_MDIO (M16)
- MDIO_CPSW (R7) to MDIO0_MDIO (M16)
- MDIO_CPSW (R8) to MDIO0_MDIO (M16)
- MDIO_CPSW (R9) to MDIO0_MDIO (M16)
- MDIO_CPSW (R10) to MDIO0_MDIO (M16)
- MDIO_CPSW (R11) to MDIO0_MDIO (M16)
- MDIO_CPSW (R12) to MDIO0_MDIO (M16)
- MDIO_CPSW (R13) to MDIO0_MDIO (M16)
- MDIO_CPSW (R14) to MDIO0_MDIO (M16)
- MDIO_CPSW (R15) to MDIO0_MDIO (M16)
- MDIO_CPSW (R16) to MDIO0_MDIO (M16)
- MDIO_CPSW (R17) to MDIO0_MDIO (M16)
- MDIO_CPSW (R18) to MDIO0_MDIO (M16)
- MDIO_CPSW (R19) to MDIO0_MDIO (M16)
- MDIO_CPSW (R20) to MDIO0_MDIO (M16)
- MDIO_CPSW (R21) to MDIO0_MDIO (M16)
- MDIO_CPSW (R22) to MDIO0_MDIO (M16)
- MDIO_CPSW (R23) to MDIO0_MDIO (M16)
- MDIO_CPSW (R24) to MDIO0_MDIO (M16)
- MDIO_CPSW (R25) to MDIO0_MDIO (M16)
- MDIO_CPSW (R26) to MDIO0_MDIO (M16)
- MDIO_CPSW (R27) to MDIO0_MDIO (M16)
- MDIO_CPSW (R28) to MDIO0_MDIO (M16)
- MDIO_CPSW (R29) to MDIO0_MDIO (M16)
- MDIO_CPSW (R30) to MDIO0_MDIO (M16)
- MDIO_CPSW (R31) to MDIO0_MDIO (M16)
- MDIO_CPSW (R32) to MDIO0_MDIO (M16)
- MDIO_CPSW (R33) to MDIO0_MDIO (M16)
- MDIO_CPSW (R34) to MDIO0_MDIO (M16)
- MDIO_CPSW (R35) to MDIO0_MDIO (M16)
- MDIO_CPSW (R36) to MDIO0_MDIO (M16)
- MDIO_CPSW (R37) to MDIO0_MDIO (M16)
- MDIO_CPSW (R38) to MDIO0_MDIO (M16)
- MDIO_CPSW (R39) to MDIO0_MDIO (M16)
- MDIO_CPSW (R40) to MDIO0_MDIO (M16)
- MDIO_CPSW (R41) to MDIO0_MDIO (M16)
- MDIO_CPSW (R42) to MDIO0_MDIO (M16)
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- MDIO_CPSW (R44) to MDIO0_MDIO (M16)
- MDIO_CPSW (R45) to MDIO0_MDIO (M16)
- MDIO_CPSW (R46) to MDIO0_MDIO (M16)
- MDIO_CPSW (R47) to MDIO0_MDIO (M16)
- MDIO_CPSW (R48) to MDIO0_MDIO (M16)
- MDIO_CPSW (R49) to MDIO0_MDIO (M16)
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- MDIO_CPSW (R64) to MDIO0_MDIO (M16)
- MDIO_CPSW (R65) to MDIO0_MDIO (M16)
- MDIO_CPSW (R66) to MDIO0_MDIO (M16)
- MDIO_CPSW (R67) to MDIO0_MDIO (M16)
- MDIO_CPSW (R68) to MDIO0_MDIO (M16)
- MDIO_CPSW (R69) to MDIO0_MDIO (M16)
- MDIO_CPSW (R70) to MDIO0_MDIO (M16)

C

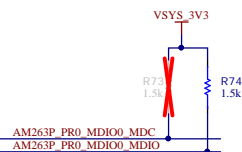


Orderable: LP-AM263P		Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024	
TID #: N/A		Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title:	
SVN Rev: Not in version control		Assembly Name: 001	Sheet: 12 of 27
Drawn by:		File: PROC171_AM263P_7_RGMII_Mil SchDoc	Size: B
Engineer: Shrinivas		Contact:	
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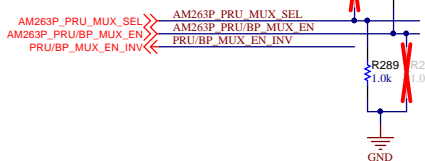
AM263P PR0 PRU0 and PRU1

ICSSM MDIO Pull-Up

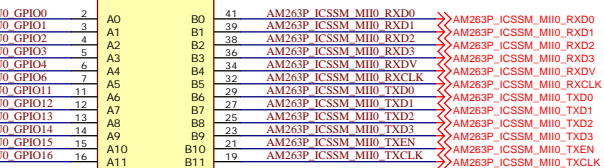
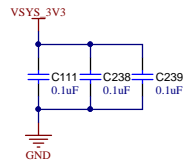


EN	SEL1	SEL2	FUNCTION
L	X	X	A_0 to A_{11} , B_0 to B_{11} , and C_0 to C_{11} are Hi-Z
H	L	L	A_0 to $A_5 = B_0$ to B_5 and A_6 to $A_{11} = B_6$ to B_{11}
H	L	H	A_0 to $A_5 = B_0$ to B_5 and A_6 to $A_{11} = C_0$ to C_{11}
H	H	L	A_0 to $A_5 = C_0$ to C_5 and A_6 to $A_{11} = B_6$ to B_{11}
H	H	H	A_0 to $A_6 = C_0$ to C_6 and A_7 to $A_{11} = C_7$ to C_{11}

MUX Default Pulls

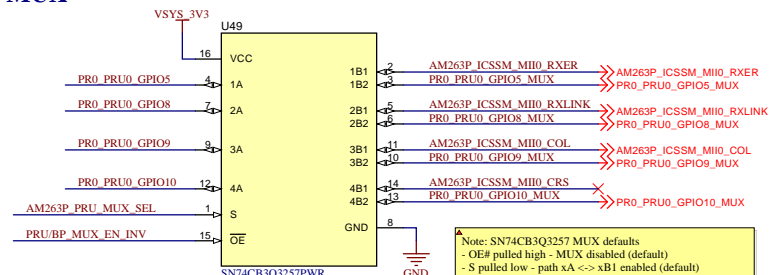


MUX Decoupling



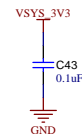
Note: TS3DDR MUX defaults

- EN# pulled high - MUX disabled (default)
- SEL pulled low - path A <-> B enabled (default)



MUX Decoupling

INPUTS		INPUT/OUTPUT A	FUNCTION
OE	S		
L	L	B1	A port = B1 port
L	H	B2	A port = B2 port
H	X	Z	Disconnect

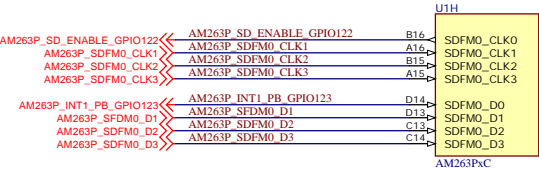


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TID #: N/A	Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 13 of 27
Drawn By:	File: PROC171_AM263P_8_PRU.SchDoc	Size: B
Engineer: Shrinivas	Contact:	



AM263P SDFM

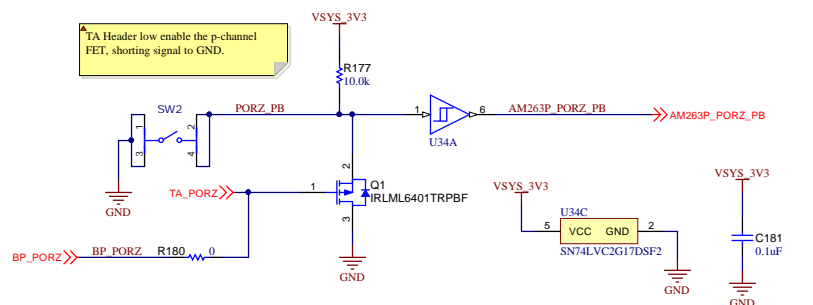


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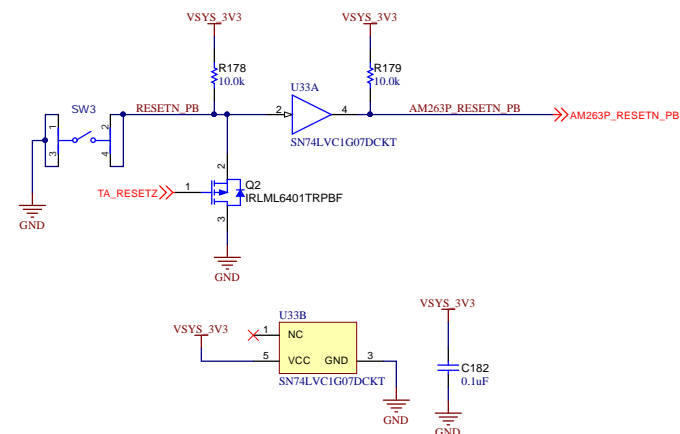
Orderable: LP-AM263P		Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024	
TID #: N/A		Project Title: AM263P Launchpad	
Number: PROC171		Rev: A	Sheet Title:
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 14 of 27
Drawn By:		File: PROC171_AM263P_9_SDFM.SchDoc	Size: B
Engineer: Shrinivas		Contact:	http://www.ti.com

Push-Buttons

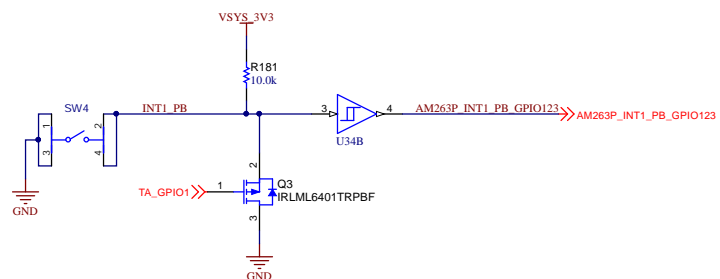
PORZ Push-Button and Test Automation




RESETZ Push-Button and Test Automation



INT1 Push-Button and Test Automation

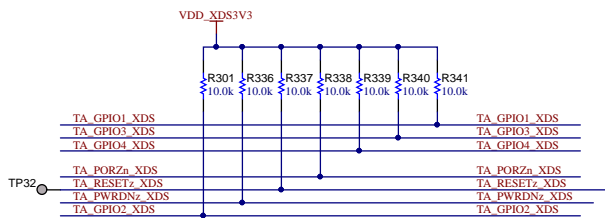
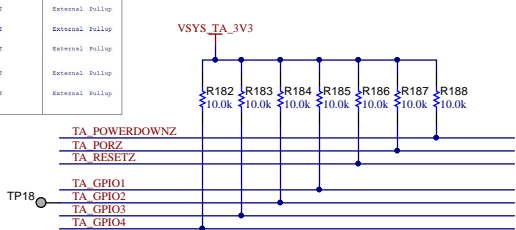


Orderable: LP-AM263P		Designed for: TEXAS INSTRUMENTS		Mod. Date: 23-08-2024	
TID #: N/A		Project Title: AM263P Launchpad			
Number: PROC171		Rev: A		Sheet Title:	
SVN Rev: Not in version control		Assembly Name: 001		Sheet: 15 of 27	
Drawn by:		File: PROC171_Push_Buttons_SchDoc			Size: B
Engineer: Shrinivas		Contact:			http://www.ti.com 

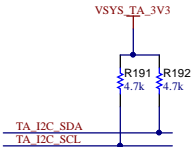
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SIGNAL NAME	DESCRIPTION	Direction	HW	UFG	Internal/External	PCIE State
TA_POWERDOWNZ	Used to Power down the system	Output	00FF0F		External Pullup	
TA_PORZn	Used to Reset the Soc PORn	Output	00FF0F		External Pullup	
TA_RESETn	SOC Reset	Output	00FF0F		External Pullup	
TA_WFI01	Interrupts to SOC	Output	00FF0F		External Pullup	
TA_WFI02	Used to Enable or Disable 1.2V Regulator	Output	00FF0F		External Pullup	
TA_WFI03	Used to Enable the BOOTMODE Buffer	Output	00FF0F		External Pullup	
TA_WFI04	Used Reset Bootmode IO Exp	Output	00FF0F		External Pullup	

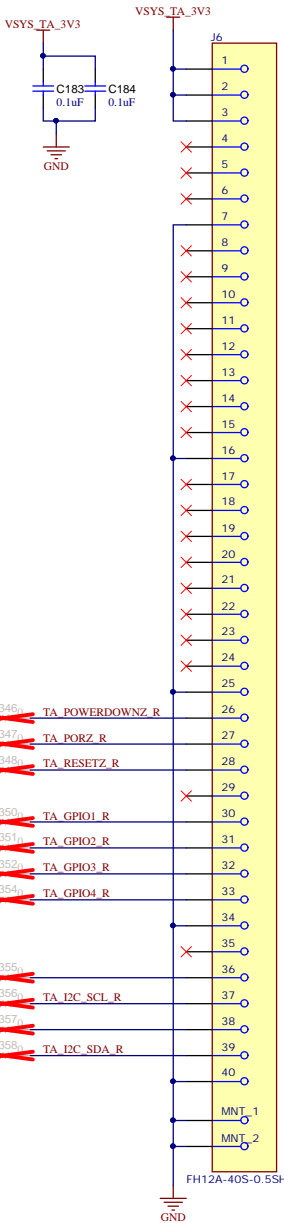
Test Automation Section



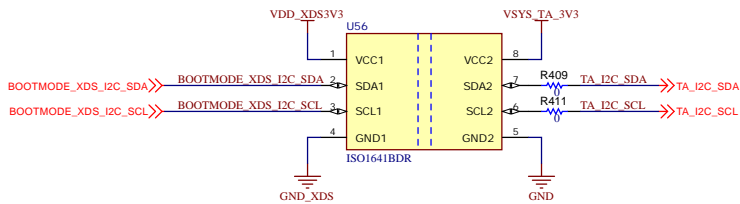
TA_I2C Pull-Up



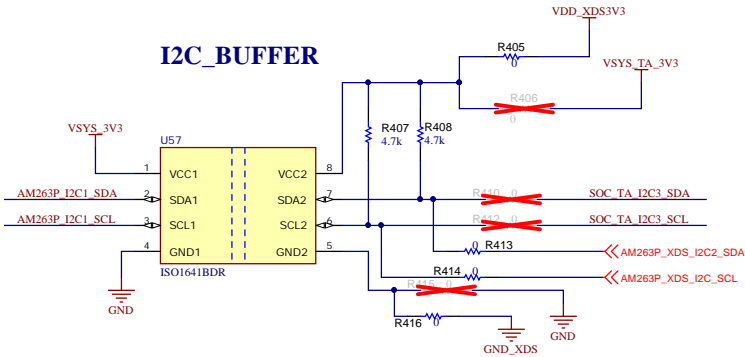
Test Automation Header



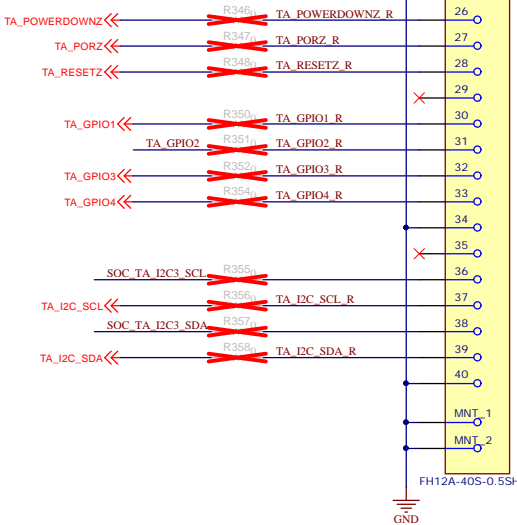
BOOTMODE_I2C_TA BUFFER



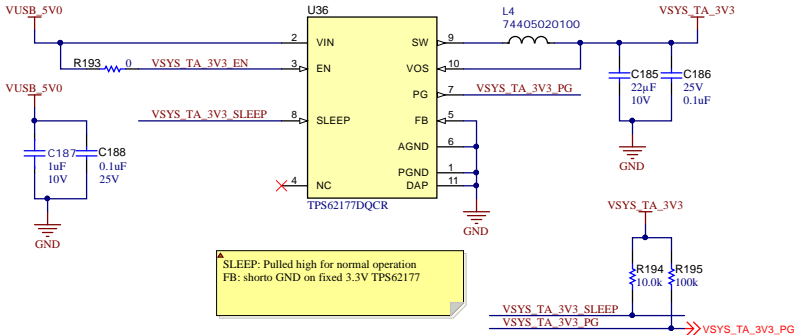
I2C_BUFFER



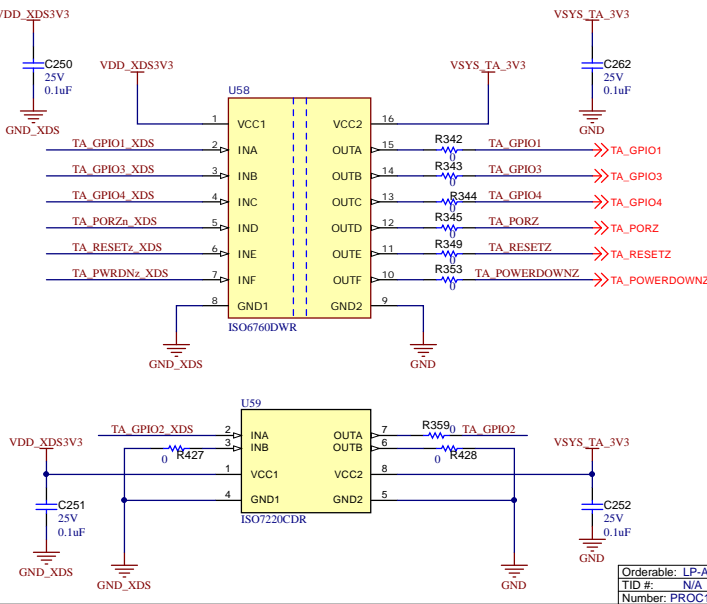
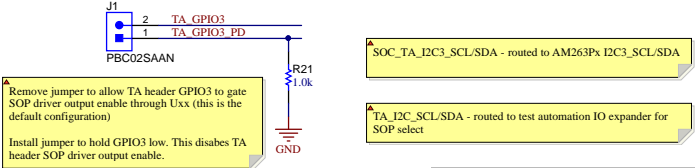
ISOLATION BUFFERS FOR TA SIGNALS



Test Automation 3.3V, 500mA Supply



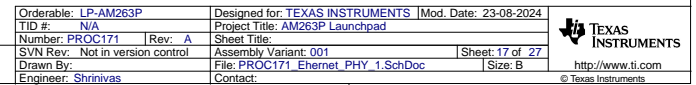
Test-Automation PORz Override



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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: Assembly Variant: 001
Drawn By: Shrinivas	File: PROC171_Test_Automation.SchDoc
Engineer: Shrinivas	Contact: Size: B
	Sheet 16 of 27
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RGMII/MII PHY



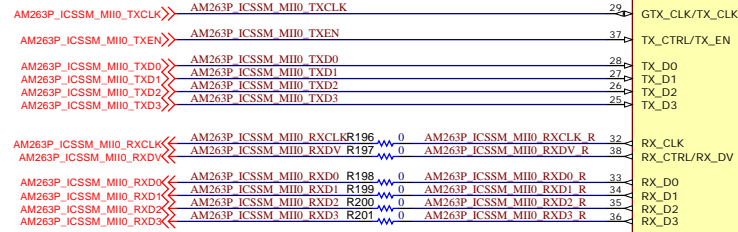
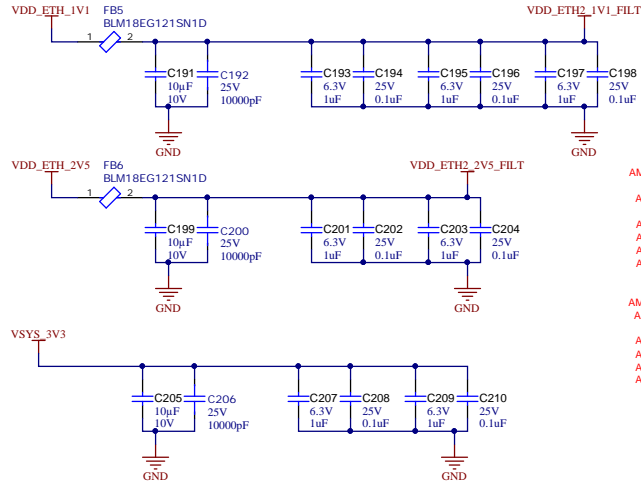
AM263P Ethernet PHY #2 - CPSW RGMII2/ICSSM_MII0

ETH1_CLKOUT >> ETH1_CLKOUT
MDIO_CLOCK >> MDIO_CLOCK
MDIO_DATA >> MDIO_DATA

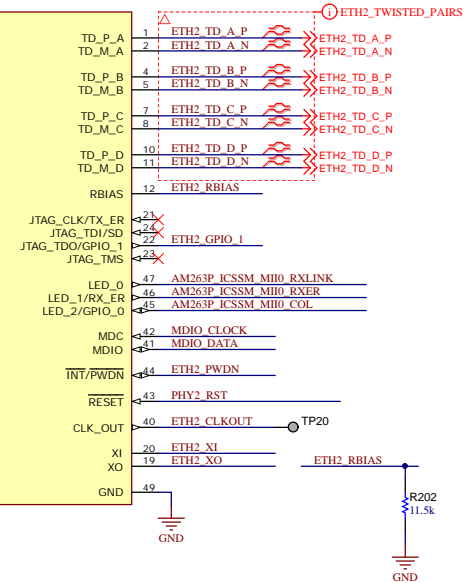
AM263P_ICSSM_MII0_RXER >> AM263P_ICSSM_MII0_RXER
AM263P_ICSSM_MII0_RXLINK >> AM263P_ICSSM_MII0_RXLINK
AM263P_ICSSM_MII0_COL >> AM263P_ICSSM_MII0_COL

Note: CPSW / ICSSM Pinmux On AM263Px
- selects between the CPSW RGMII2/MII2 interfaces and the PRU1 ICSSM MII0 interfaces

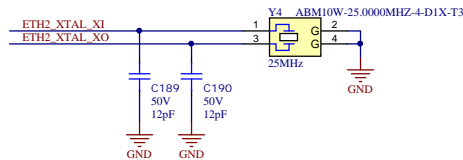
PHY Decoupling



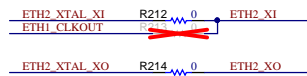
RGMII/MII PHY



25 MHz Crystal

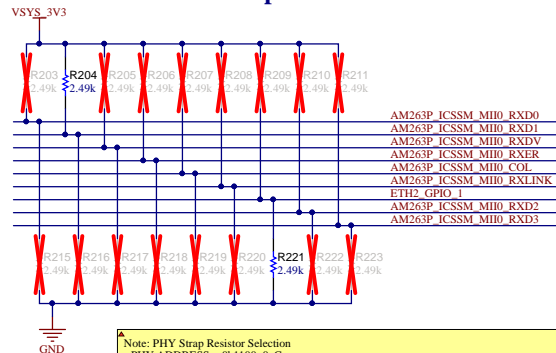


Oscillator Input Selection



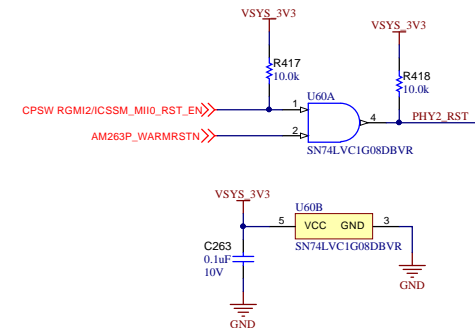
Note: PHY Oscillator Input Selection
- (default) select 25 MHz XTAL input to ETH2_XI/XO
- optional select to disable 25 MHz XTAL and input CLK_OUT from PHY1

PHY Strap Resistors



Note: PHY Strap Resistor Selection
- PHY ADDRESS = 0b1100, 0xC
- Auto-negotiation, 10/100/1000 advertised, Auto-MDI-X
- RGMII to Copper (100Base-T/100Base-TX/10Base-Te)

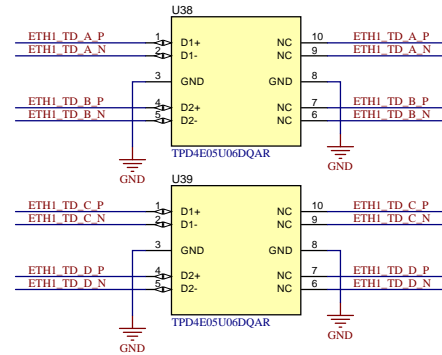
PHY_RST_LOGIC



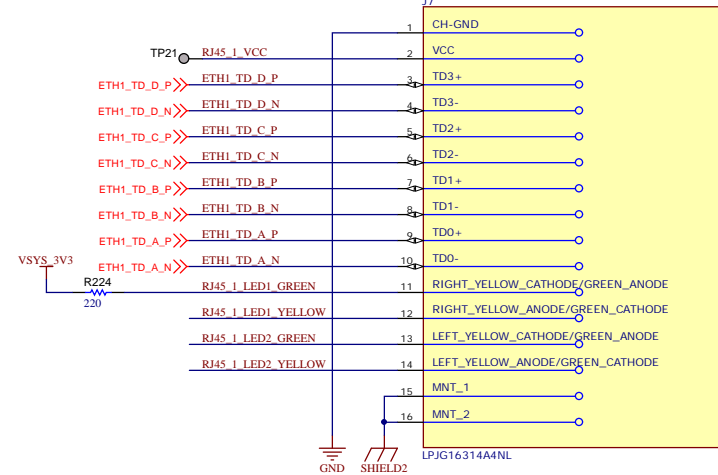
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AM263P Ethernet PHY #1 - RJ-45 Jack

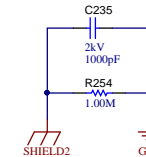
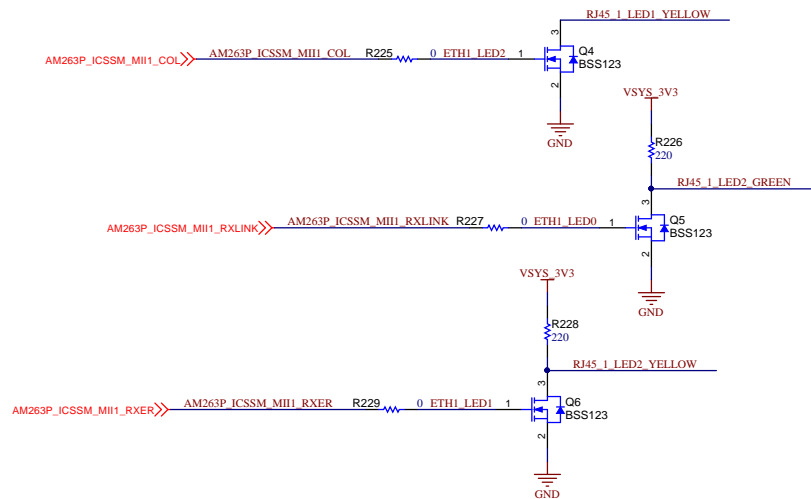
RJ-45 ESD Protection



RJ-45 Jack #1



RJ-45 LED Drivers

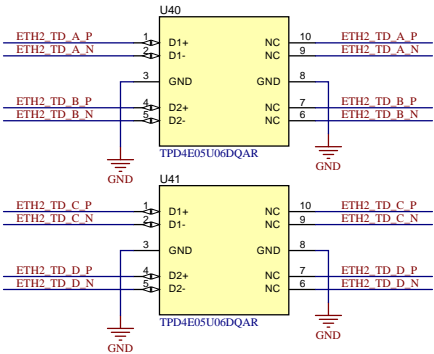


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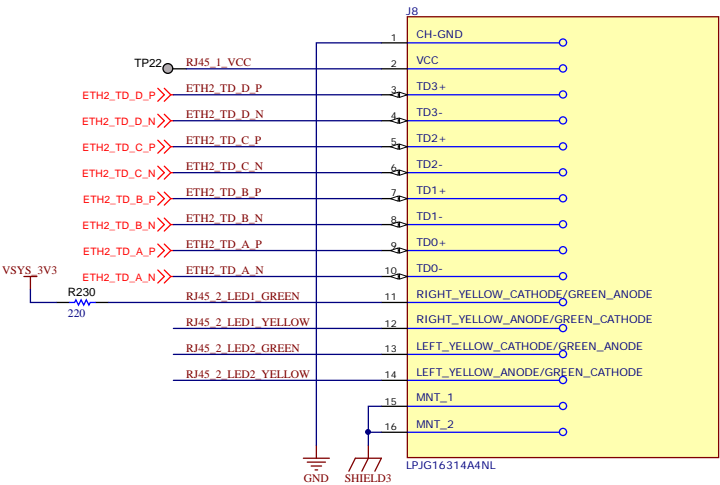
Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: Assembly Variant: 001
Drawn By: Shrinivas	File: PROC171_Ethernet_RJ45_1.SchDoc
Engineer: Shrinivas	Contact: Sheet: 19 of 27
	Size: B
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AM263P Ethernet PHY #2 - RJ-45 Jack

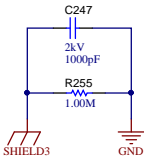
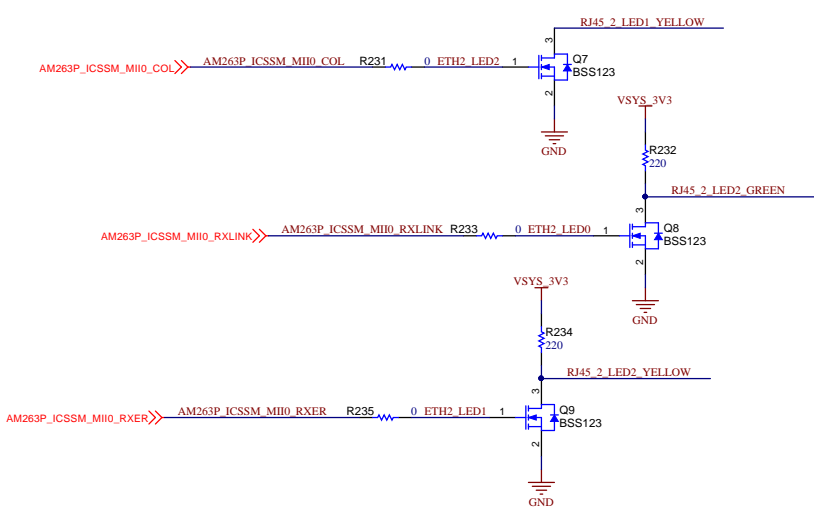
RJ-45 ESD Protection



RJ-45 Jack #2



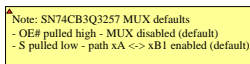
RJ-45 LED Drivers



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Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title:
Drawn By:	Assembly Variant: 001
Engineer: Shrinivas	File: PROC171_Ethernet_RJ45_2.SchDoc
	Contact:

Boosterpack Site 1



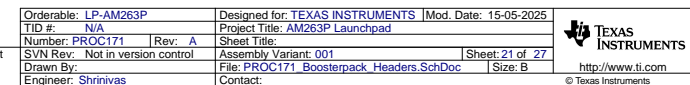
Pin	Signal	Function
1	AM263P_SPI0_CLK	PR0_PRU0_GPIO18_MUX
2	AM263P_SPI0_CLK	PR0_PRU0_GPIO16_MUX
3	AM263P_SPI0_D1	PR0_PRU0_GPIO14_MUX
4	AM263P_SPI0_D1	PR0_PRU0_GPIO14_MUX
5	AM263P_SPI0_D0	PR0_PRU0_GPIO13_MUX
6	AM263P_SPI0_D0	PR0_PRU0_GPIO13_MUX
7	AM263P_SPI0_CS0	PR0_PRU0_GPIO8_MUX
8	AM263P_SPI0_CS0	PR0_PRU0_GPIO8_MUX
9	AM263P_BP_MUX_SEL	
10	PRU/BP_MUX_EN_INV	

INPUTS		INPUT/OUTPUT A	FUNCTION
OE	S		
L	L	B1	A port = B1 port
L	H	B2	A port = B2 port
H	X	Z	Disconnect



EN	SEL1	SEL2	FUNCTION
L	X	X	A_0 to A_{15} , B_0 to B_{11} , and C_0 to C_{11} are Hi-Z
H	L	L	A_0 to A_2 = B_0 to B_2 and A_6 to A_{11} = B_6 to B_{11}
H	L	H	A_0 to A_6 = B_0 to B_6 and A_6 to A_{11} = C_6 to C_{11}
H	H	L	A_0 to A_6 = C_0 to C_6 and A_6 to A_{11} = B_6 to B_{11}
H	H	H	A_0 to A_6 = C_0 to C_6 and A_6 to A_{11} = C_6 to C_{11}

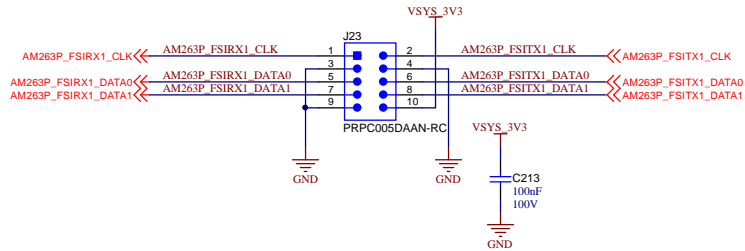
AM263P_IEP0_EDIO_DATA_IN_OUT31	AM263P_IEP0_EDIO_DATA_IN_OUT31
AM263P_IEP0_EDC_SYNC_OUT0	AM263P_IEP0_EDC_SYNC_OUT0



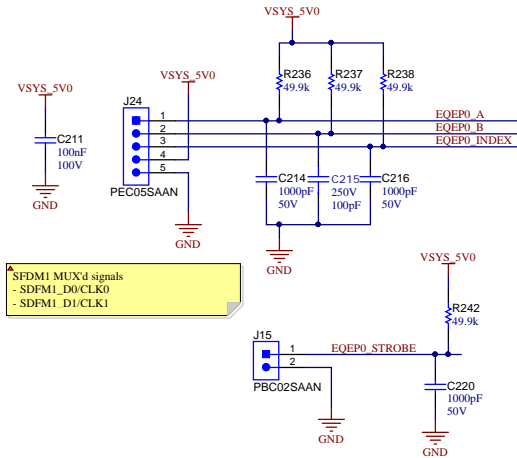
Breakout Headers

FSI Header

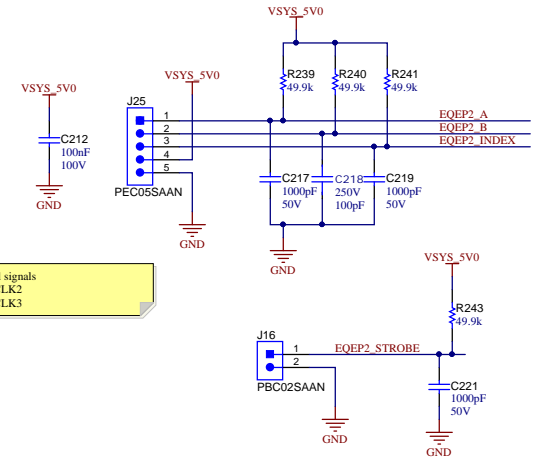
C2000 LP Style FSI Breakout



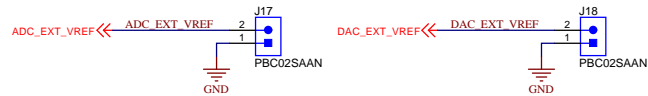
eQEP0/SFDM1 Headers



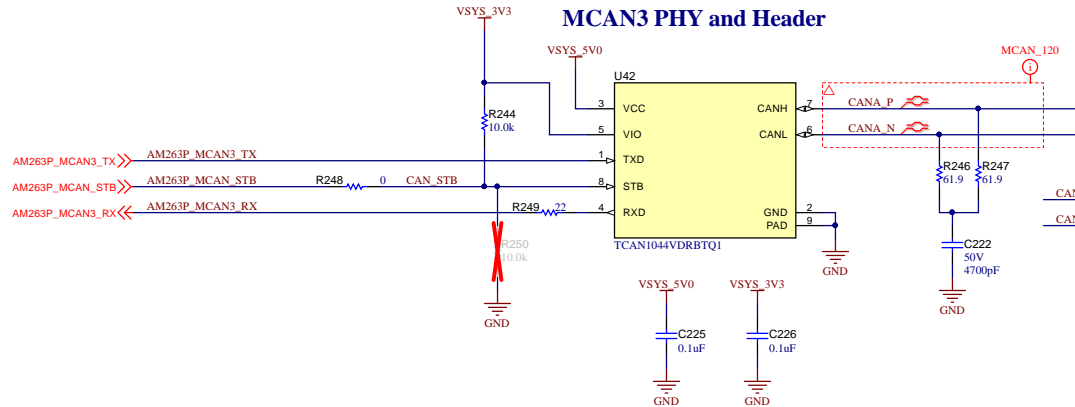
eQEP2/SFDM2 Headers



ADC/DAC External VREF Header

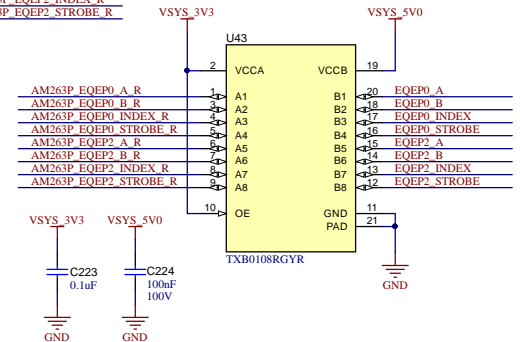


MCAN3 PHY and Header



AM263P_EQEP0_A	AM263P_EQEP0 A	R328	33.2	AM263P_EQEP0 A R
AM263P_EQEP0_B	AM263P_EQEP0 B	R329	33.2	AM263P_EQEP0 B R
AM263P_EQEP0_INDEX	AM263P_EQEP0 INDEX	R330	33.2	AM263P_EQEP0 INDEX R
AM263P_EQEP0_STROBE	AM263P_EQEP0 STROBE	R331	33.2	AM263P_EQEP0 STROBE R
AM263P_EQEP2_A	AM263P_EQEP2 A	R332	33.2	AM263P_EQEP2 A R
AM263P_EQEP2_B	AM263P_EQEP2 B	R333	33.2	AM263P_EQEP2 B R
AM263P_EQEP2_INDEX	AM263P_EQEP2 INDEX	R334	33.2	AM263P_EQEP2 INDEX R
AM263P_EQEP2_STROBE	AM263P_EQEP2 STROBE	R335	33.2	AM263P_EQEP2 STROBE R

eQEP Bi-Directional Level Translator



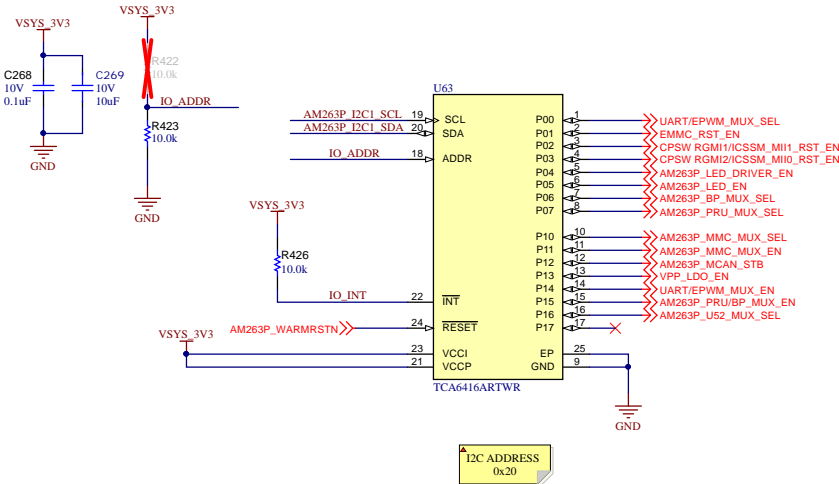
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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Assembly Variant: 001
Drawn By: Engineer: Shrinivas	File: PROC171_Breakout_Headers.SchDoc
	Sheet: 22 of 27
	Size: B
	Contact:



IO_EXPANDER

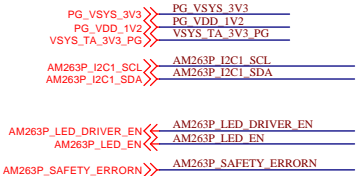
AM263P_I2C1_SCL >> AM263P_I2C1_SCL
AM263P_I2C1_SDA >> AM263P_I2C1_SDA



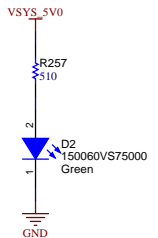
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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS	Mod. Date: 10-03-2025
TID #: N/A	Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title: *
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 23 of 27
Drawn By:	File: PROC171_IO_EXPANDER.SchDoc	Size: B
Engineer: Shrinivas	Contact:	http://www.ti.com

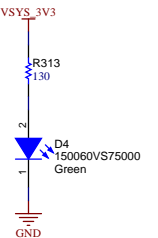
System LED Indicators



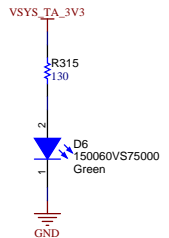
System 5.0V



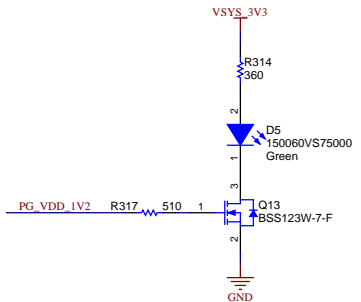
System 3.3V



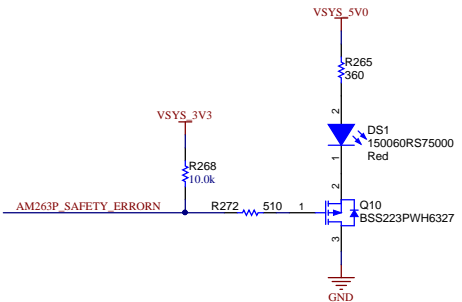
Test Automation 3.3V



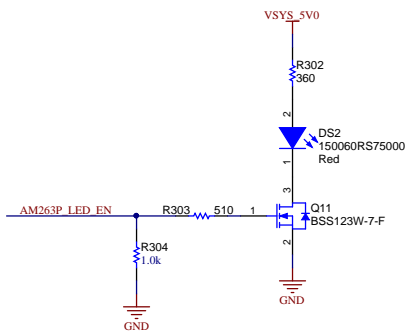
AM263P 1.2V



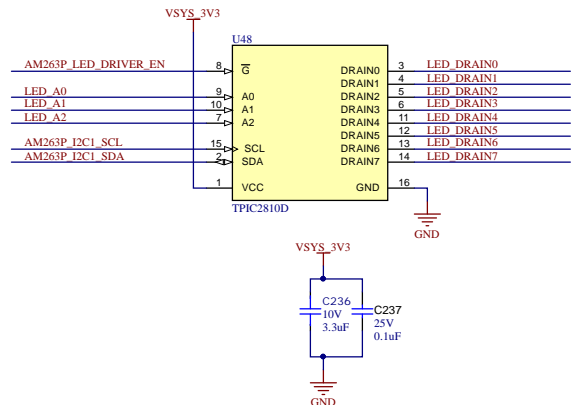
AM263P Safety Error



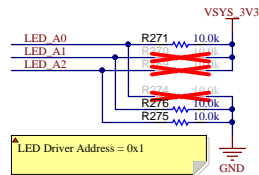
AM263P GPIO LED



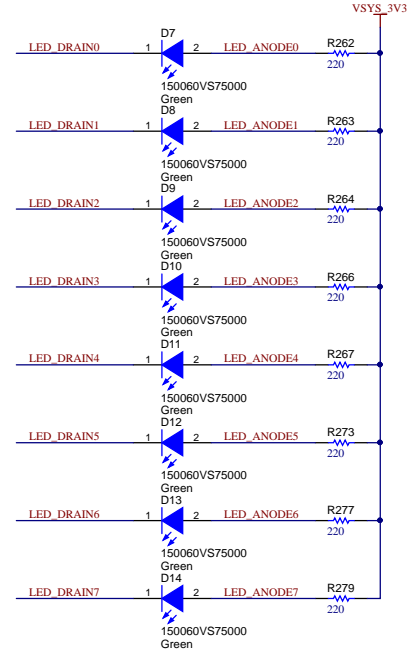
Industrial LED Driver



LED Driver Address



LED Driver Enable

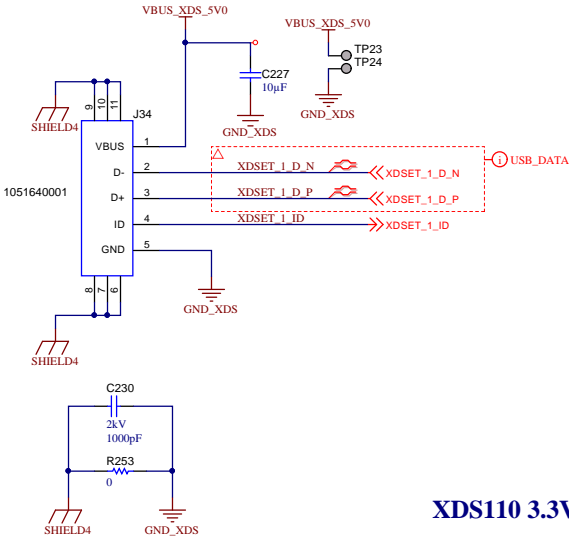


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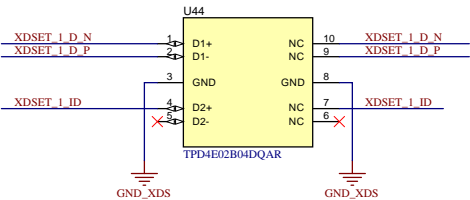
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TID #: N/A	Project Title: AM263P Launchpad
Number: PROC171	Rev: A
SVN Rev: Not in version control	Sheet Title: Assembly Variant: 001
Drawn By: Shrinivas	File: PROC171_LED_SchDoc
Engineer: Shrinivas	Contact: Sheet: 24 of 27
	Size: B
	http://www.ti.com
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XDS110 JTAG/USB-to-UART Bridge

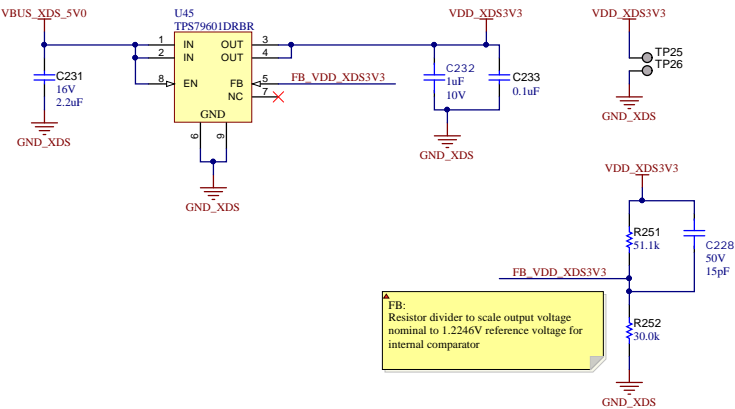
XDS110 USB Micro-B PORT



USB Mini-B ESD Protection



XDS110 3.3V LDO

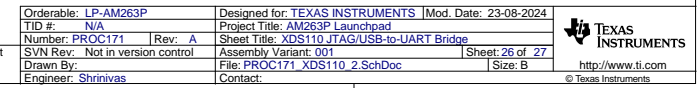


FB:
Resistor divider to scale output voltage
nominal to 1.2246V reference voltage for
internal comparator

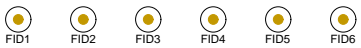
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Orderable: LP-AM263P	Designed for: TEXAS INSTRUMENTS	Mod. Date: 23-08-2024
TID #: N/A	Project Title: AM263P Launchpad	
Number: PROC171	Rev: A	Sheet Title: XDS110 JTAG/USB-to-UART Bridge
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 25 of 27
Drawn By:	File: PROC171_XDS110_1.SchDoc	Size: B
Engineer: Shrinivas	Contact:	

XDS110 JTAG/USB-to-UART Bridge



System Hardware, Notes, Labels



PCB Number: PROC171
PCB Rev: A

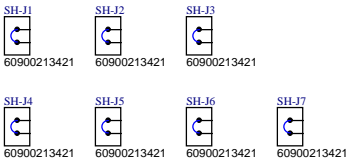
PCB
LOGO
Texas Instruments



PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

Selection Jumpers



LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20"

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: LP-AM263P		Designed for: TEXAS INSTRUMENTS Mod. Date: 23-08-2024	
TID #:	N/A	Project Title: AM263P Launchpad	
Number:	PROC171	Rev:	A
SVN Rev:	Not in version control	Assembly Variant:	001
Drawn By:		File:	PROC171_Hardware.SchDoc
Engineer:	Shrinivas	Contact:	
		Sheet:	27 of 27
		Size:	B

Board Stack Report